

Seat
No.

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मन - 040

Microprocessor - II
(1010)

P. Pages : 2

Time : Three Hours

Max. Marks : 100

Instructions to Candidates :

1. Do not write anything on question paper except Seat No.
2. Answersheet should be written with blue ink only. Graph or diagram should be drawn with the same pen being used for writing paper or black HB pencil.
3. Students should note, no supplement will be provided.
4. Attempt **any two** sub questions from each questions.
5. Assume suitable data wherever necessary.
6. Diagrams / sketches should be given wherever necessary.
7. Figures to the right indicate full marks.

1. a) Discuss I/O mapped I/O and memory mapped I/O in short. 10
b) Explain MS DOS boot Record (Boot Sector) in detail. 10
c) Draw and discuss any two modes of 8253 / 54 with appropriate waveform. 10
2. a) Set the 8251 in asynchronous mode as a transmitter and receiver with even parity enabled, 2 stop bits 8-bit character length, frequency 160 KHz and baud rate 10K. Write 8086 program to initialise 8251 for above configuration. 10
b) Draw and discuss stepper motor interfacing using 8255. 10
c) Explain the use of various signals in centronics parallel printer Interface. 10
3. a) Draw and explain a general block diagram of 8275 CRT controller. 10
b) Discuss Color Graphics Adapter (CGA) in detail. 10
c) Draw and discuss Bus Arbitration Logic and Wait state Logic in short. 10
4. a) Draw the format of Device Control Block (DCB) of HDC and explain it. 10
b) Discuss EISA and VESA bus in short. 10

- c) Explain following signals of 8272. 10
- i) WRITE PROTECT / TWO SIDE.
 - ii) INDEX.
 - iii) FAULT / TRACK. O
 - iv) DRIVE SELECT, DSO and DSI.
 - v) \overline{RW} / SEEK.
5. a) Explain addressing modes of 8051 with example. 10
- b) Discuss internal memory of 8051. 10
- c) Explain I/O Ports of 8051 microcontroller. 10
