



Computer Organization (1090)

P. Pages : 2

Time : Three Hours

Max. Marks : 100

Instructions to Candidates :

1. Do not write anything on question paper except Seat No.
2. Answer sheet should be written with blue ink only. Graph or diagram should be drawn with the same pen being used for writing paper or black HB pencil.
3. Students should note, no supplement will be provided.
4. Answer **any two** questions from each unit.
5. Draw a neat diagram and labelled wherever necessary.
6. Figures to the indicate full marks.
7. Assume suitable data if necessary.

UNIT - I

1. Discuss different Instruction formats of Machines. 10
2. Explain with example Addressing modes. 10
3. Draw a neat diagram and explain single, two Bas Architecture. 10

UNIT - II

4. Solve following by using Booths algorithm 10
i) -14×-15 ii) 11×12
5. Solve following by using Restoring algorithm. 10
i) $10 \div 5$ ii) $8 \div 2$
6. Perform multiplication by using bit pairing method 10
i) 4×5 ii) -11×10

UNIT - III

7. Write and explain control sequence for execution of complete instruction Sub (R_3), R_1 10
8. Explain and Discuss Wilkes Design with neat diagram. 10

9. Write a short note on 10
 i) Emulation ii) Bit slice Architecture

UNIT - IV

10. Draw and explain 4 - way associative cache organization. 10
11. Define and explain following terms. 10
 i) SRAM ii) Memory hierarchy
12. Explain memory interleaving ? Explain multiple memory modules in brief. 10

UNIT - V

13. Explain I/O channels in detail. 10
14. Explain & Differentiate RISC Vs CISC. 10
15. Explain PCI Bus in detail. 10
